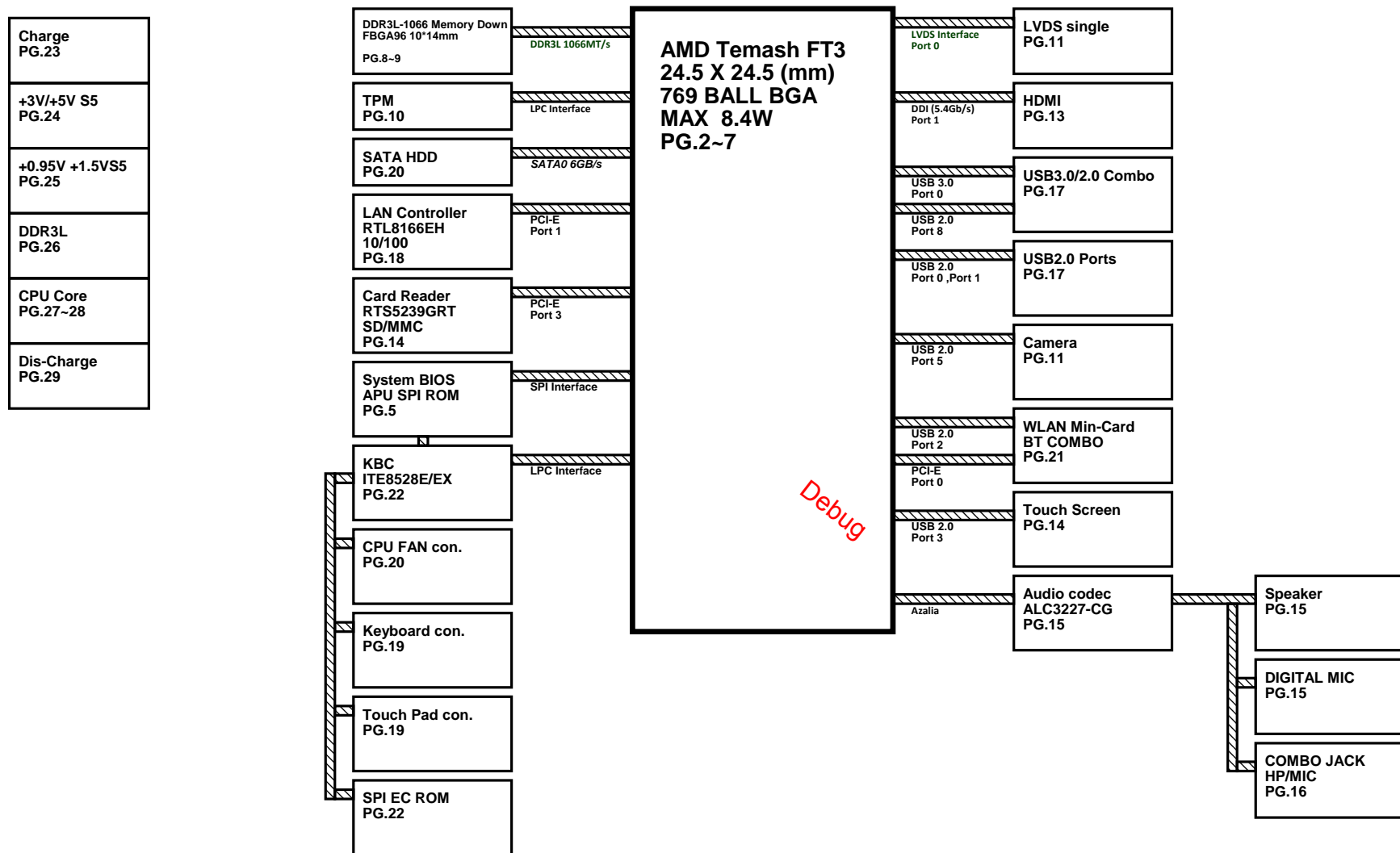



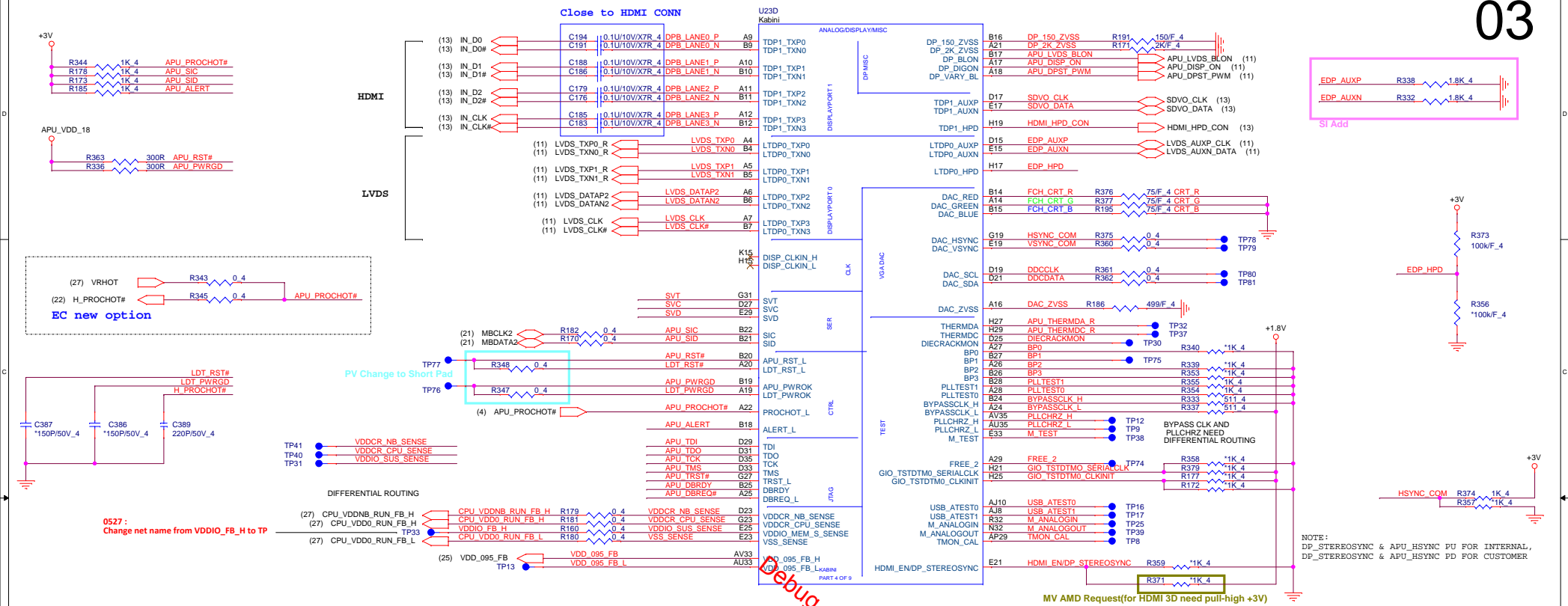
Y02 (HP Mario) AMD Temash Platform Block Diagram

01

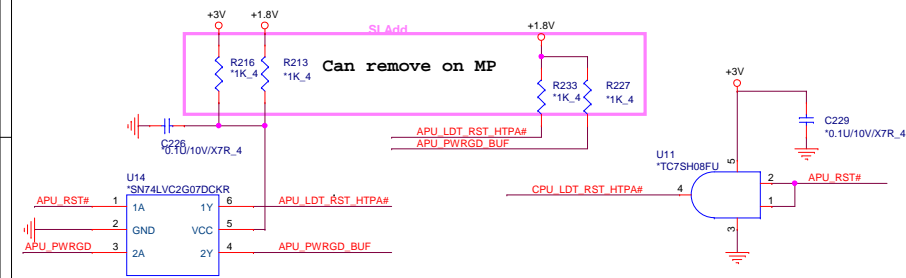


Stackup
TOP
GND
IN1
IN2
VCC
BOT

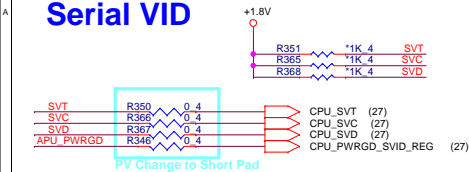
		PROJECT : Y02	
		Quanta Computer Inc.	
Size	Document Number	BLOCK DIAGRAM	
Custom		Date: Tuesday, July 02, 2013	Sheet 1 of 30



HDT(Hardware Debug Tool) Connector

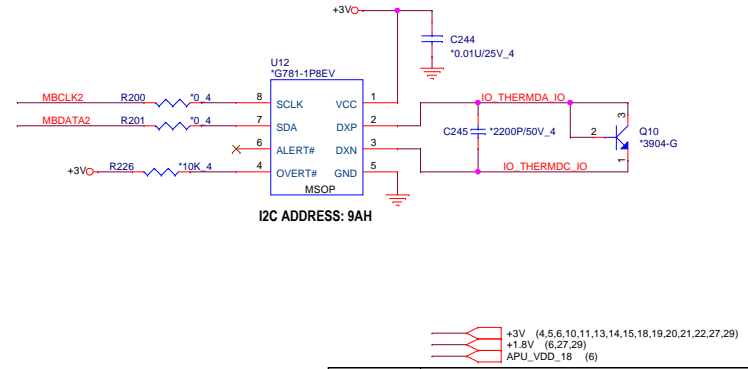


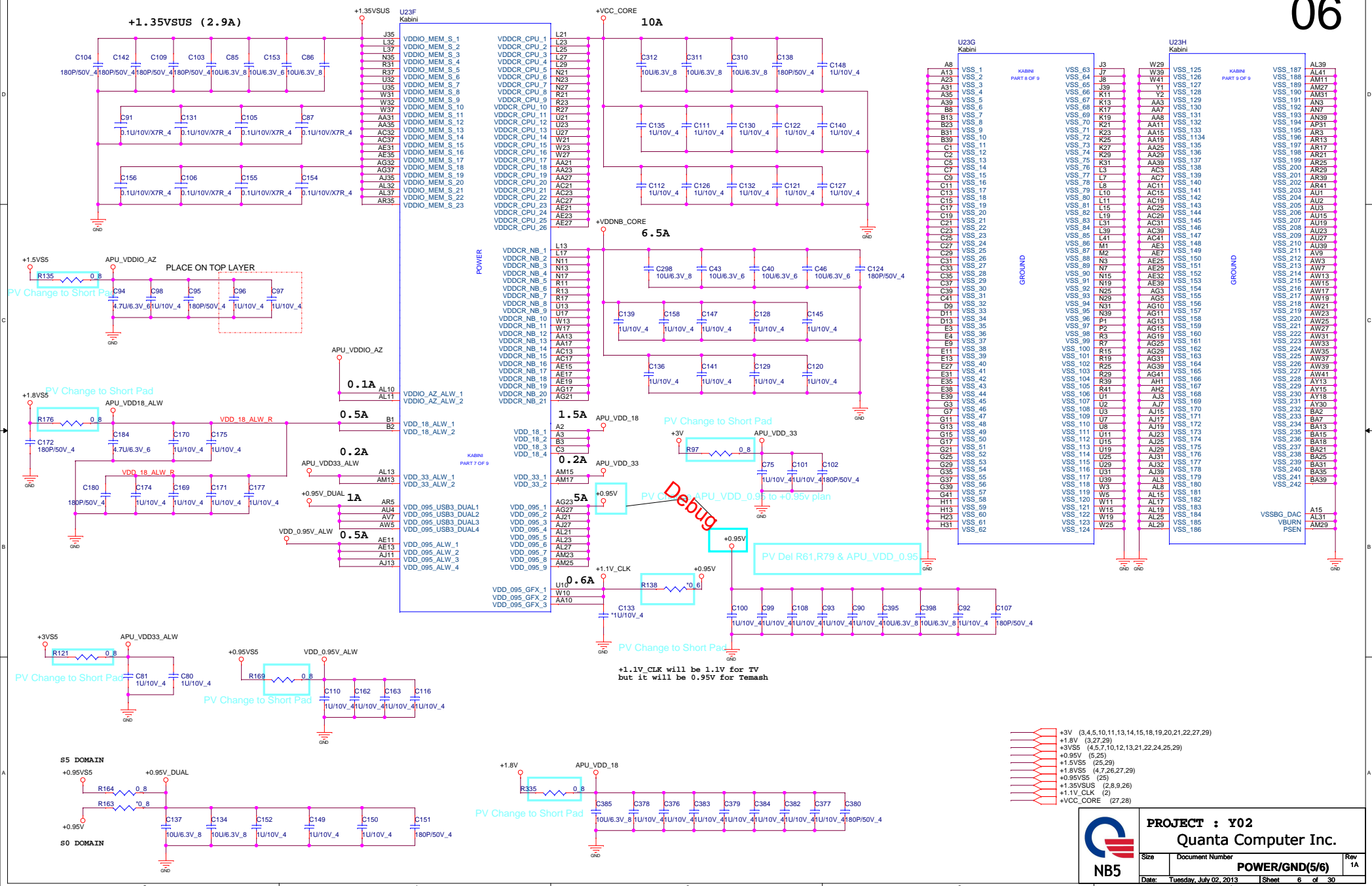
Serial VID



VFIX MODE		VID Override table (VDD)
SVC	SVD	Boot Voltage
0	0	1.1V
0	1	1.0V
1	0	0.9V
1	1	0.8V

Local Thermal Sensor



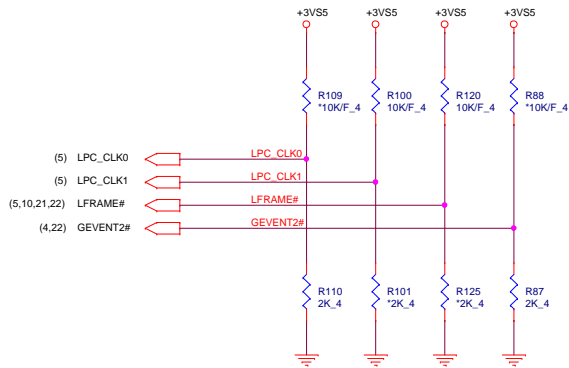




STRAPS PINS

07

OVERLAP COMMON PADS WHERE POSSIBLE FOR DUAL-OP RESISTORS.

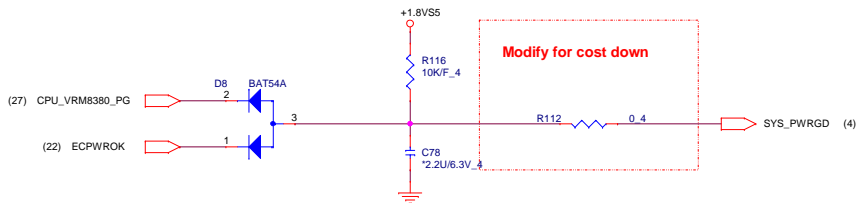


REQUIRED STRAPS

	LPC_CLK0	LPC_CLK1	LFRAME#	GEVENT2#
PULL HIGH	BOOT FAIL TIMER ENABLED	CLKGEN ENABLED DEFAULT	SPI ROM DEFAULT	1.8V SPI ROM
PULL LOW	BOOT FAIL TIMER DISABLED DEFAULT	CLKGEN DISABLED	LPC ROM	3.3V SPI ROM DEFAULT

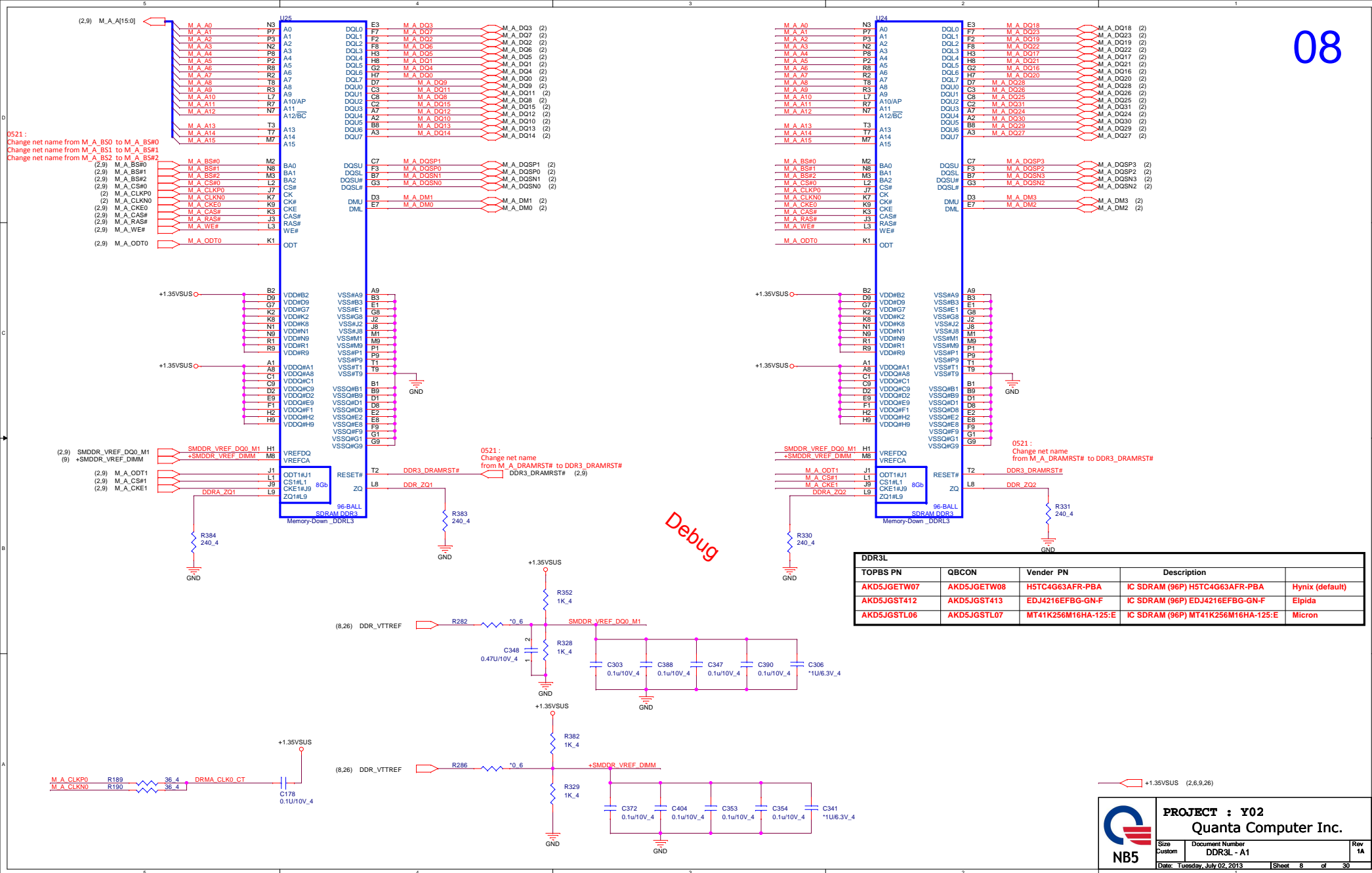
Debug

SYS_PWRGD



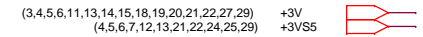
+3V (3,4,5,6,10,11,13,14,15,18,19,20,21,22,27,29)
+3VS5 (4,5,6,10,12,13,21,22,24,25,29)
+1.8VS5 (4,6,26,27,29)

 NB5	PROJECT : Y02 Quanta Computer Inc.		
	Size	Document Number STRAP (6/6)	Rev 1A
	Date: Tuesday, July 02, 2013	Sheet 7 of 30	



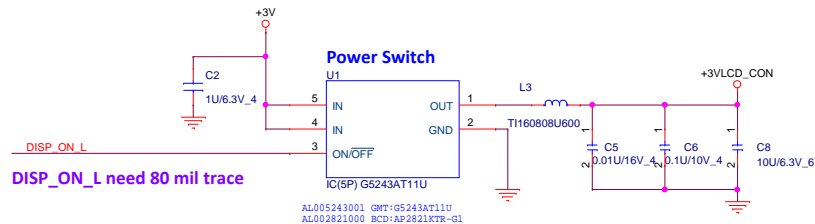
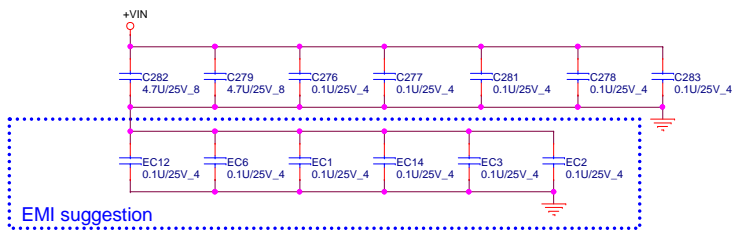
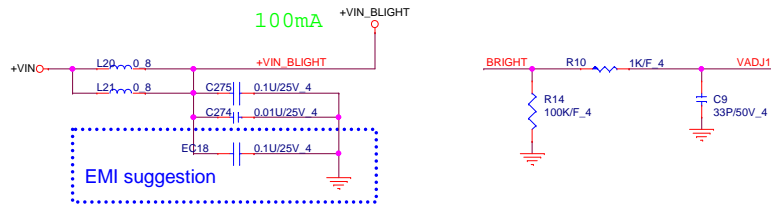
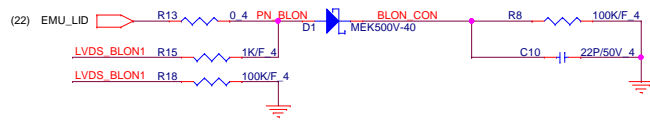
PROJECT : Y02
Quanta Computer Inc.

Size Custom	Document Number	Rev
	DDR3L - A1	1A
Date: Tuesday, July 02, 2013	Sheet	8 of 30

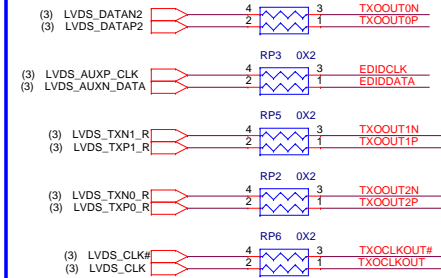


	SLB9635	SLB9656
R71	Install	No install
R46	No install	Install
R44	Install	No install
R48	Install	No install
Y1,C52,C53	Install	No install
R59	Install	No install
R47	Install	No install
R64	No install	Install
R65	No install	Install

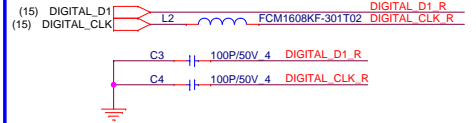
LID Switch



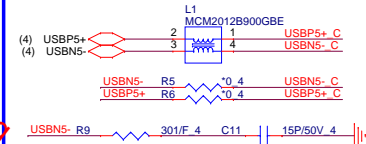
LVDS SINGLE



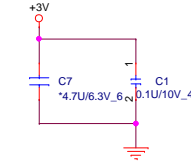
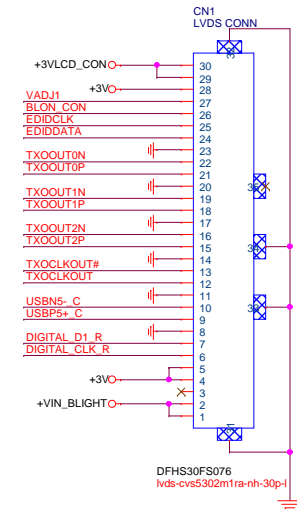
D_MIC



USB CAMERA



Debug




11

(3,4,5,6,10,13,14,15,18,19,20,21,22,27,29) +3V

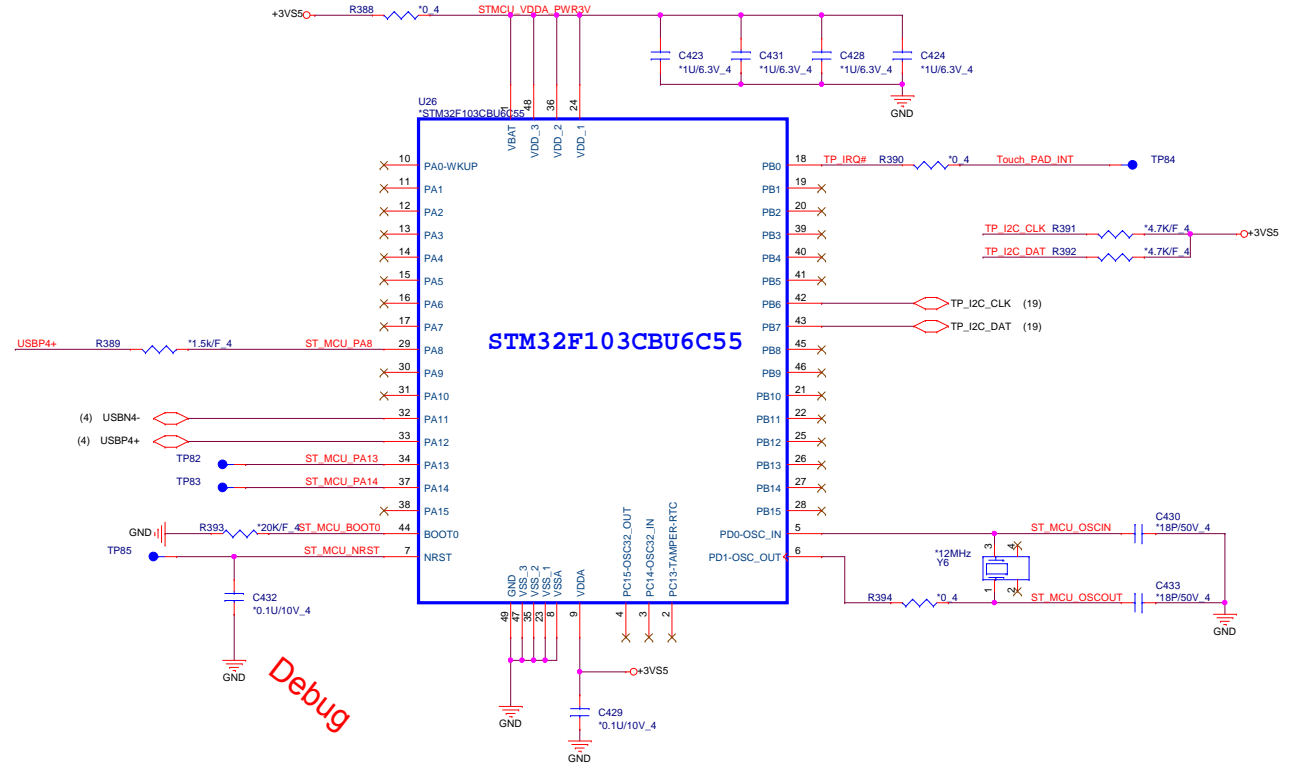
(5,13,19,21,22,23,24) +3VPCU

(13,14,15,19,20,21,29) +5V

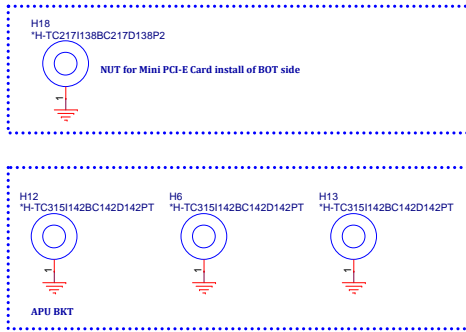
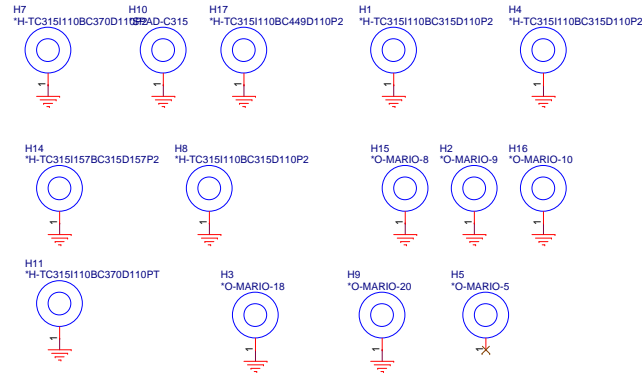
(23,24,25,26,28,29) +VIN

 NB5	PROJECT : Y02 Quanta Computer Inc.			
	Size Custom	Document Number LCD CONN/CAM/LID		Rev 1A
	Date: Tuesday, July 02, 2013	Sheet 11	of 30	

Touch PAD I2C



HOLE



PROJECT : Y02
Quanta Computer Inc.

Size	Document Number	Rev
Custom	Hole/Touch PAD I2C	1A
Date: Tuesday, July 02, 2013		Sheet 12 of 30

(4) CLK_PCIE_REQ2#



(4) CARD_PCIE_RST#



Zdiff = 100 ohm

- (2) PCIE_TXP3_CARD
(2) PCIE_TXN3_CARD
(5) CLK_PCIE_CARDP
(5) CLK_PCIE_CARDN
(2) PCIE_RXP3_CARD
(2) PCIE_RXN3_CARD

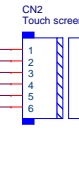
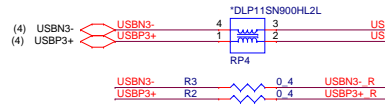
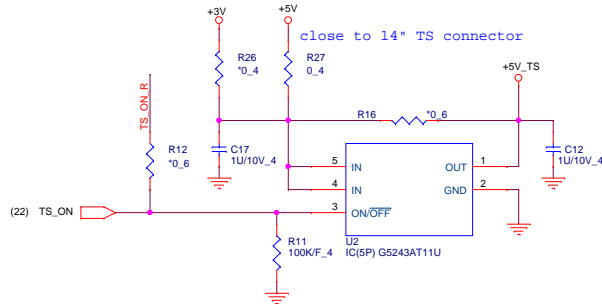
Please add 9 GND VIAs
connection with thermal PAD

need close to Chip

need close to Chip

+3V (3,4,5,6,10,11,13,15,18,19,20,21,22,27,29)
+5V (13,15,19,20,21,29)

Touch Screen Connector

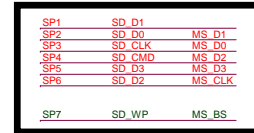


Close to chip pin

Close to chip pin

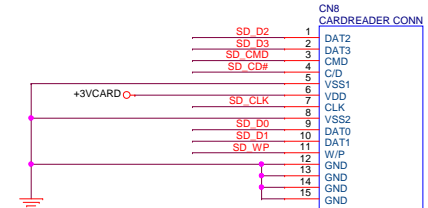
Debug

CLOSE CONN



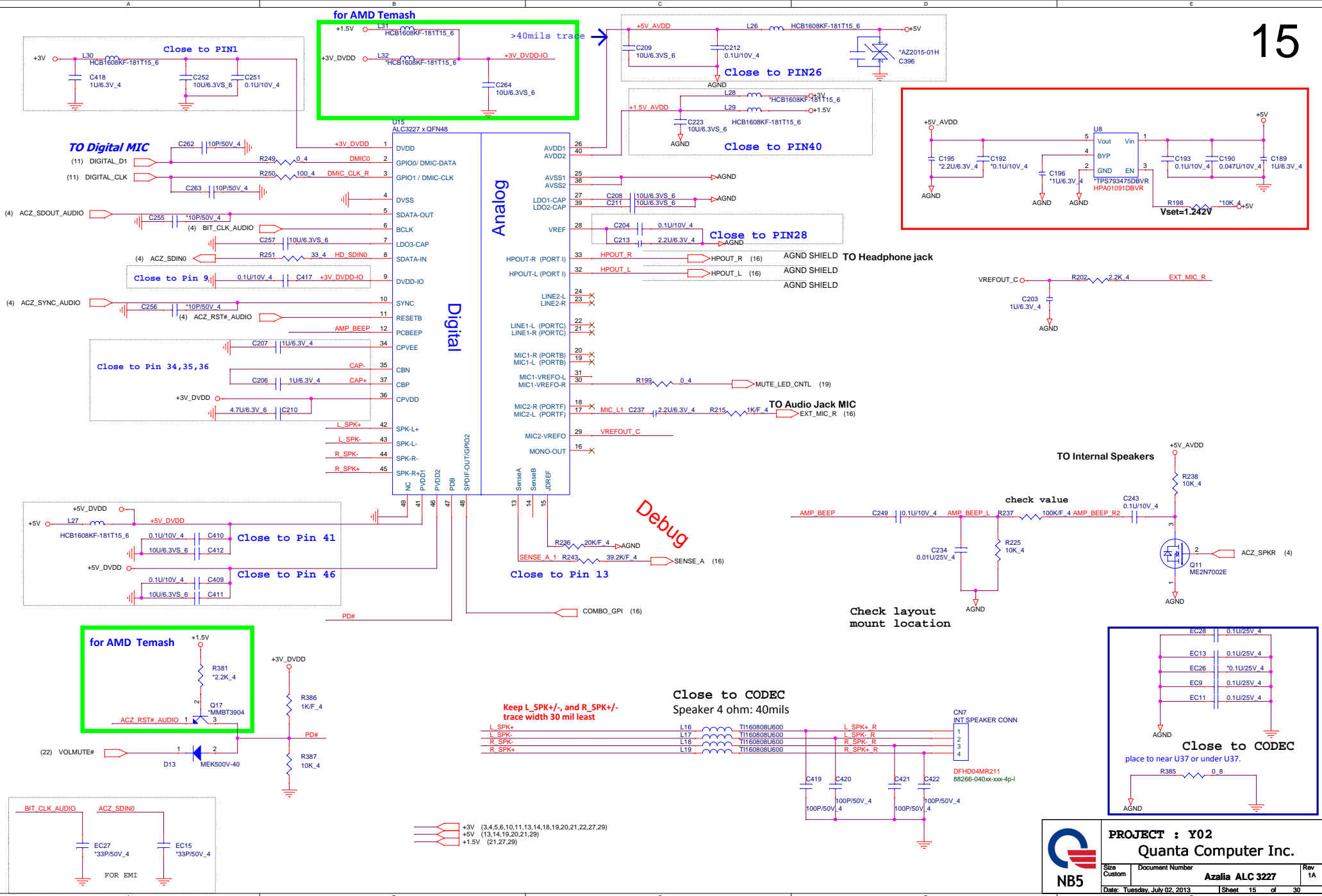
Share Pin

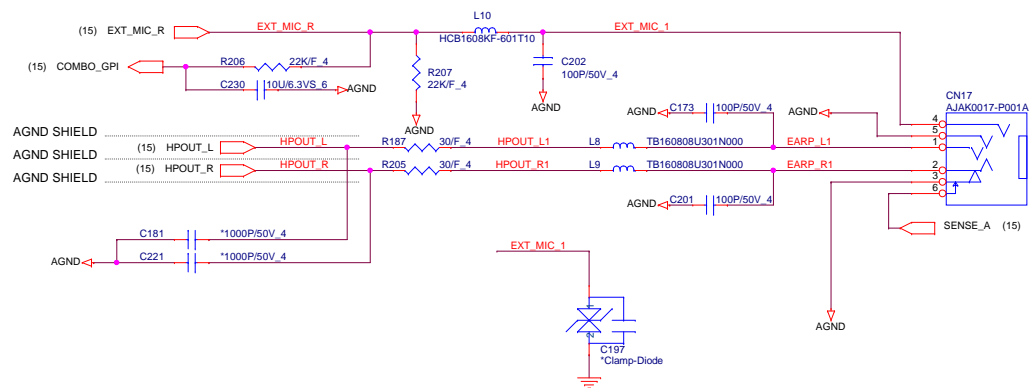
Reserve for EMI

SD / MMC
CARD READERChange footprint to
sdcards-psdbtc-09g1bsl4h3-11p

PROJECT : Y02
Quanta Computer Inc.

Size Custom Document Number CR RTS5239/Touch Scree
Date: Tuesday, July 02, 2013 Sheet 14 of 30



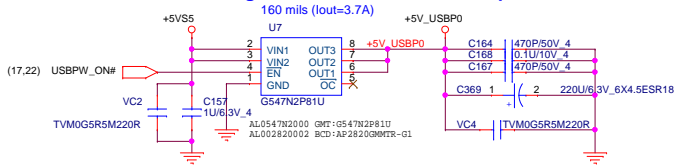


Debug

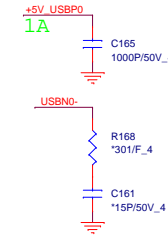
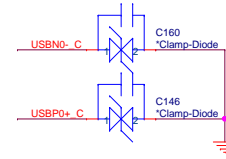
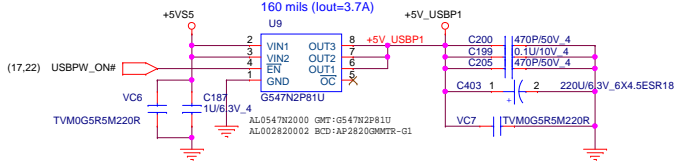
Legend: +5VSS (17,24,25,26,27,29)

 NB5	PROJECT : Y02 Quanta Computer Inc.		
	Size Custom	Document Number Audio Jack	Rev 1A
Date: Tuesday, July 02, 2013			
Sheet 16 of 30			

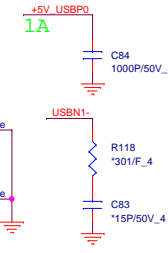
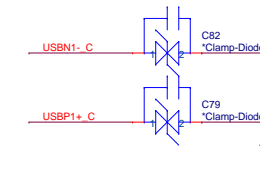
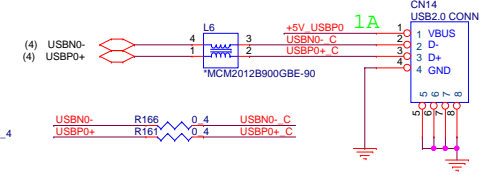
For Right side USB2.0 Connector*2pcs



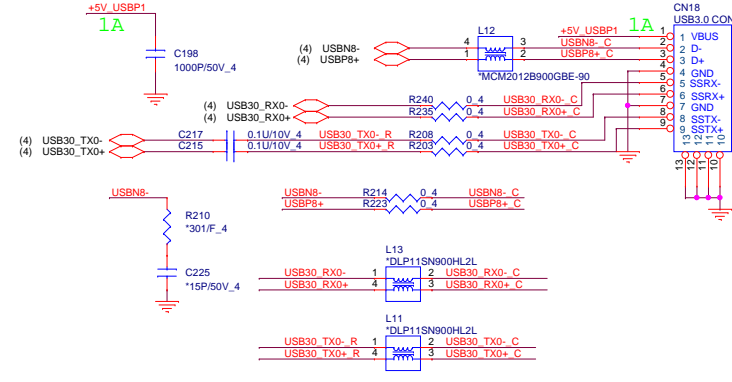
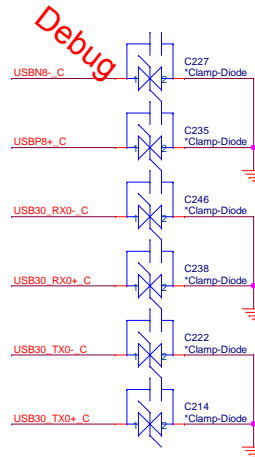
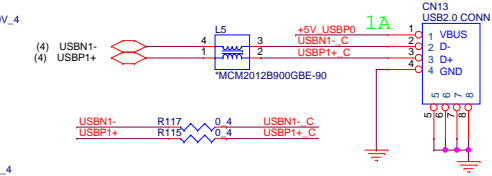
For Left side USB3.0/2.0 Combo *1pcs



Right side USB2.0 Connector



Right side USB2.0 Connector



Left side USB3.0/2.0 Combo

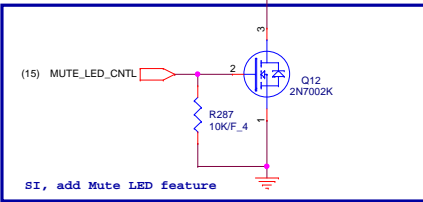
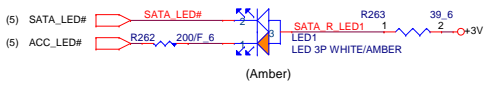
PROJECT : Y02
Quanta Computer Inc.

Size Custom	Document Number	Rev 1A
USB USB3.0/2.0		
Date: Tuesday, July 02, 2013	Sheet 17	of 30

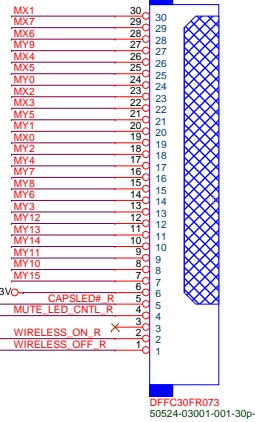
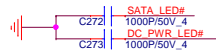
KEYBOARD Con.



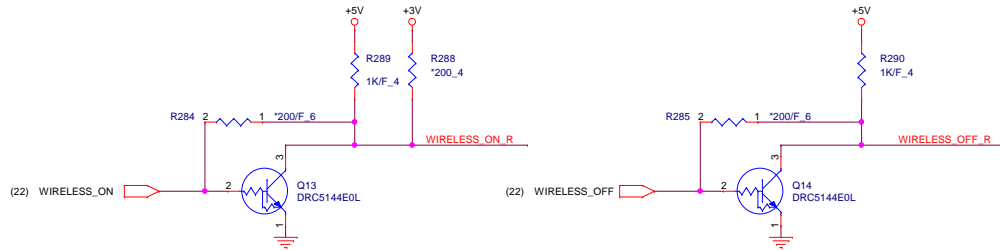
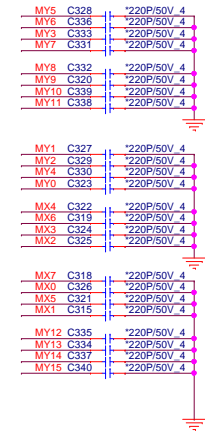
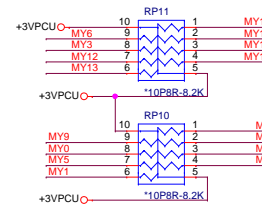
SATA_LED



PWR_LED

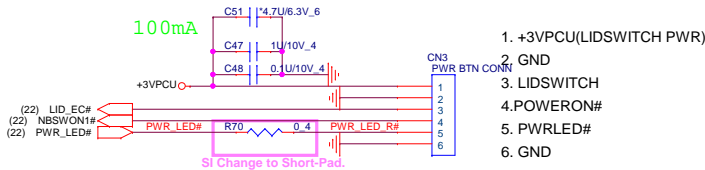


KEYBOARD PULL-UP



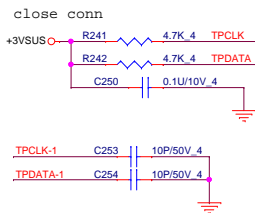
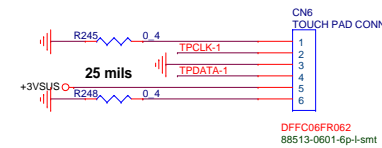
Debug

POWER BOTTON CONNECT



- 1. +3VPCU(LIDSWITCH PWR)
- 2. GND
- 3. LIDSWITCH
- 4. POWERON#
- 5. PWRLED#
- 6. GND

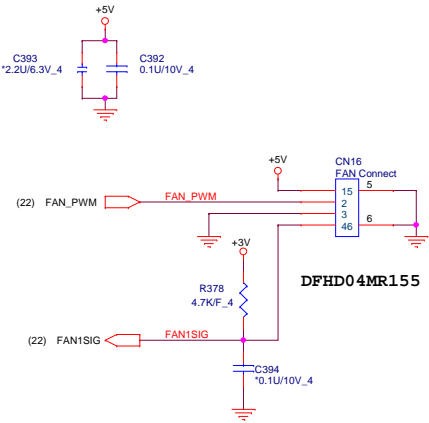
TOUCH PAD Con.



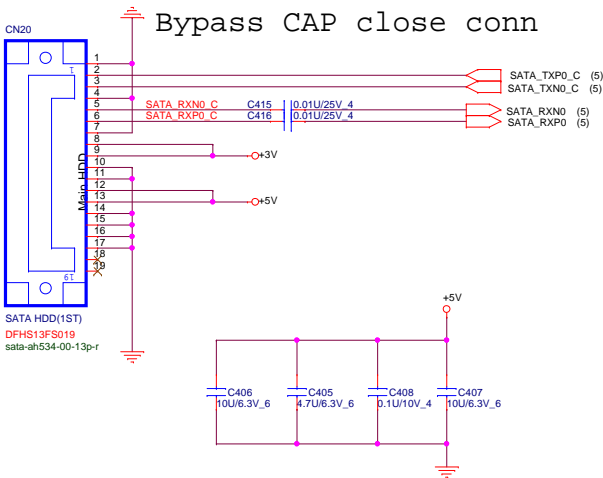
- +3V (3,4,5,6,10,11,13,14,15,18,20,21,22,27,29)
- +5V (13,14,15,20,21,29)
- +3VSUS (29)
- +3VPCU (5,13,21,22,23,24)

	PROJECT : Y02	
	Quanta Computer Inc.	
Size	Document Number	Rev
Custom	KB/SW/TP/LED	1A
Date: Tuesday, July 02, 2013	Sheet 19 of 30	

CPU FAN




SATA HDD CONNECTOR

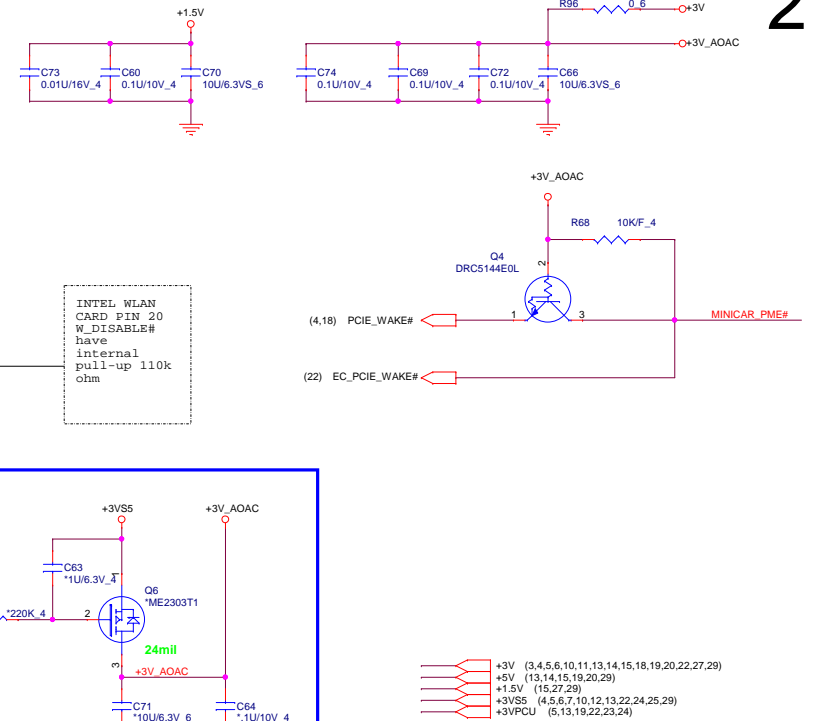
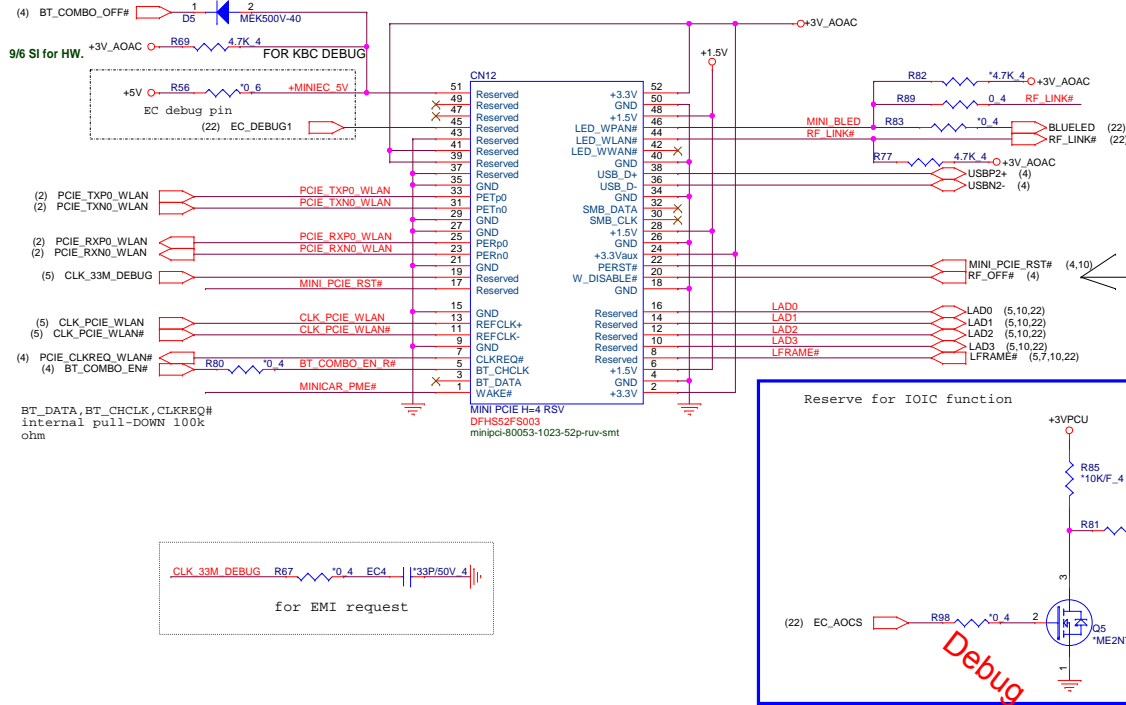


Debug

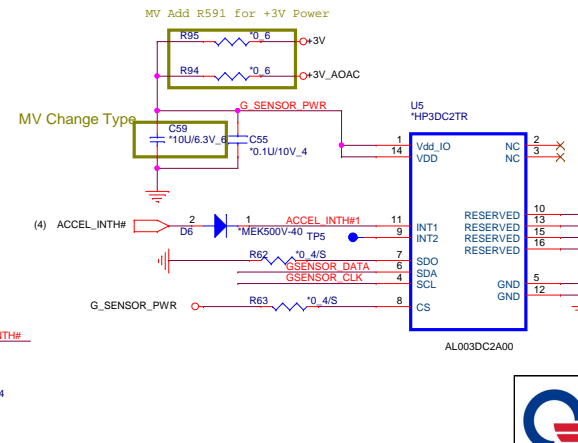
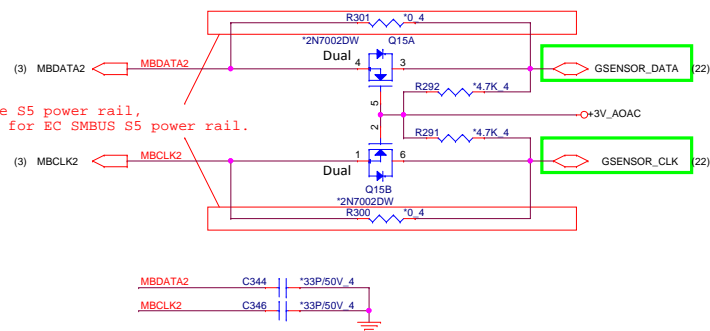
+3V (3,4,5,6,10,11,13,14,15,18,19,21,22,27,29)
+5V (1,3,14,15,19,21,29)


	PROJECT : Y02	
	Quanta Computer Inc.	
Size Custom	Document Number HDD/FAN	Rev 1A
Date: Tuesday, July 02, 2013		Sheet 20 of 30

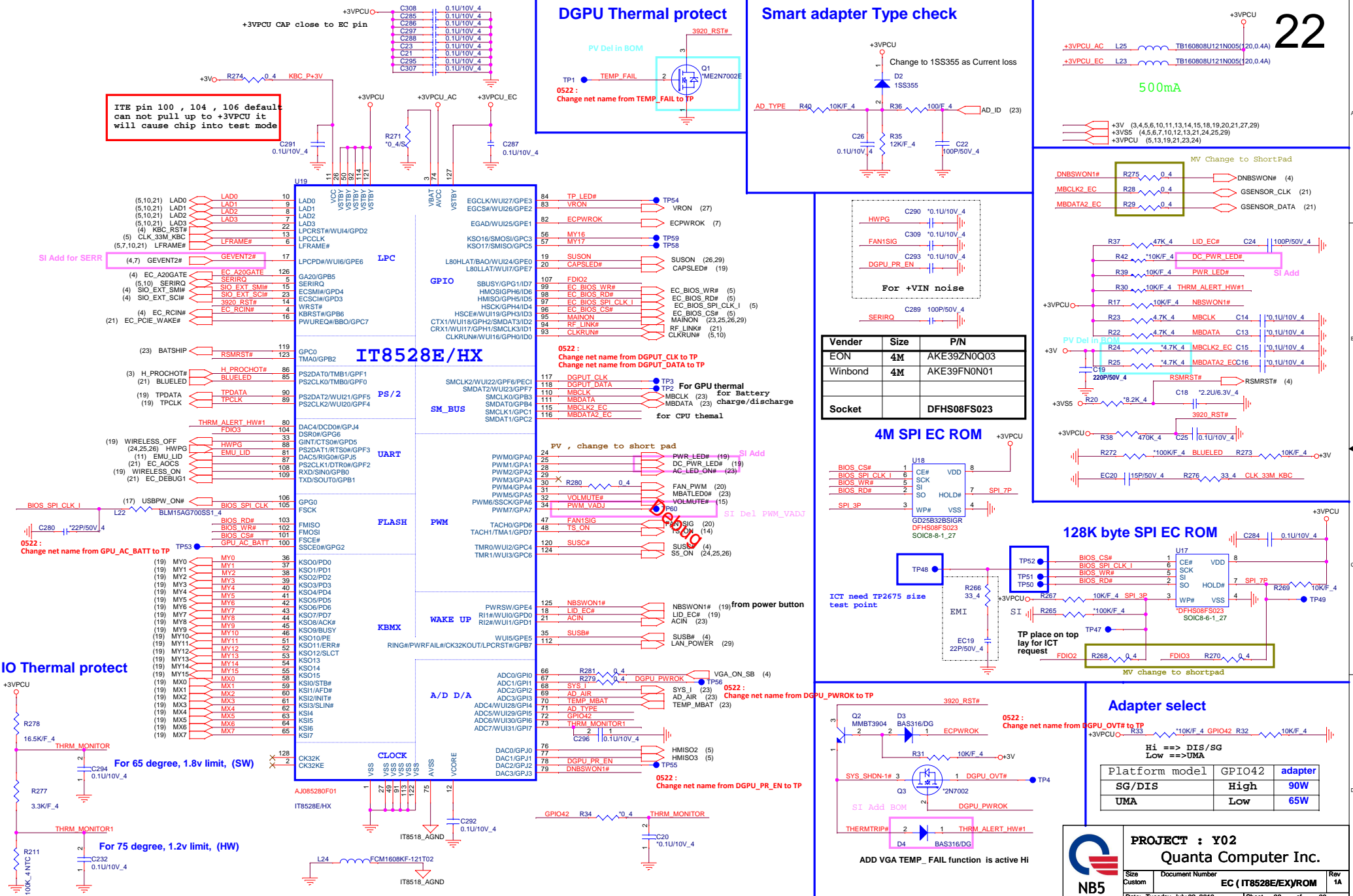
Mini PCI-E Card 1 WLAN

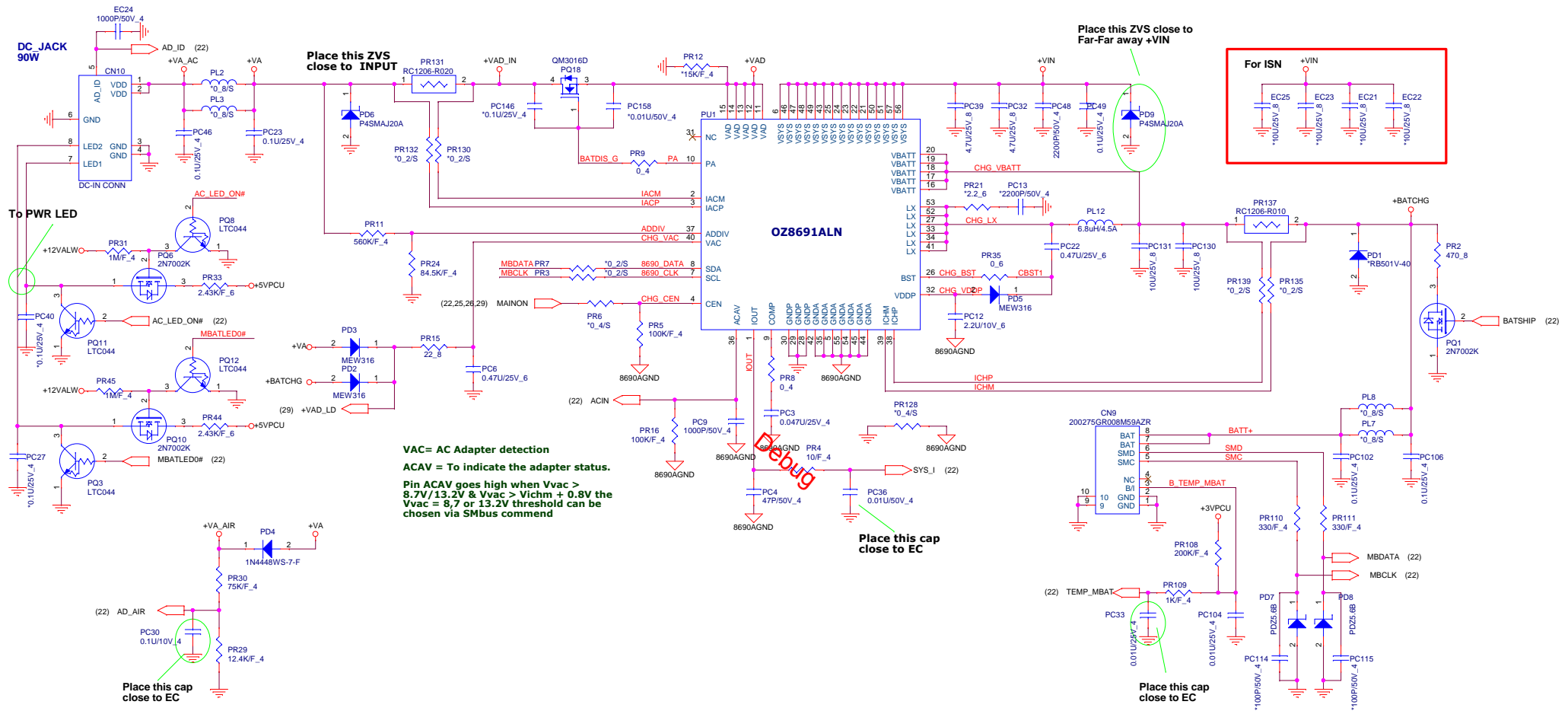



Accelerometer Sensor



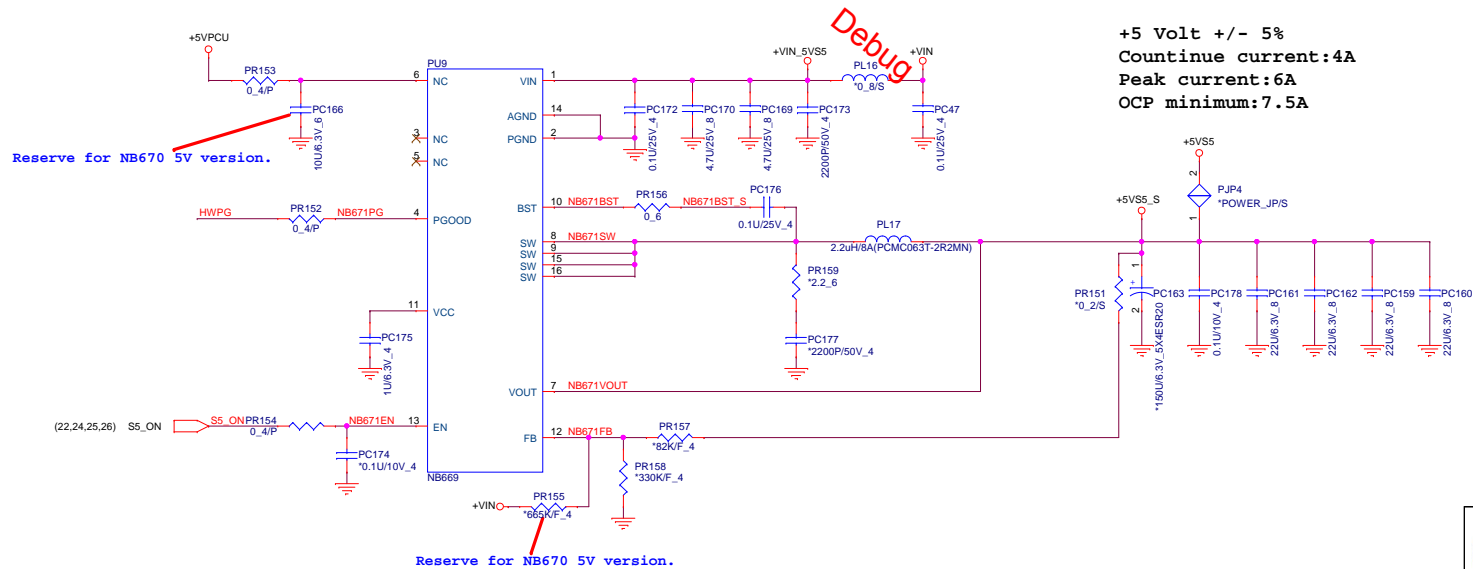
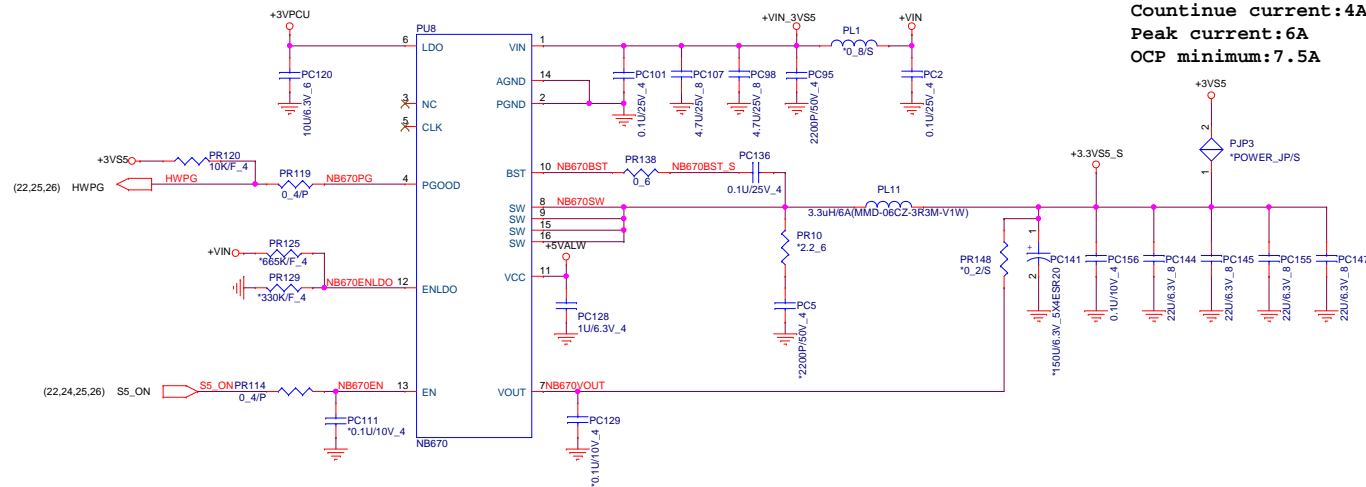
 NB5	PROJECT : Y02			Rev 1A
	Quanta Computer Inc.			
	Size Custom	Document Number MINI PCIE CONN / G-sensor		
	Date: Tuesday, July 02, 2013	Sheet 21	of 30	




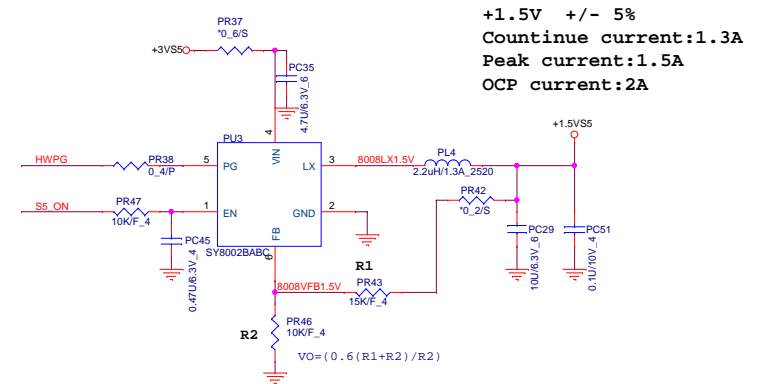
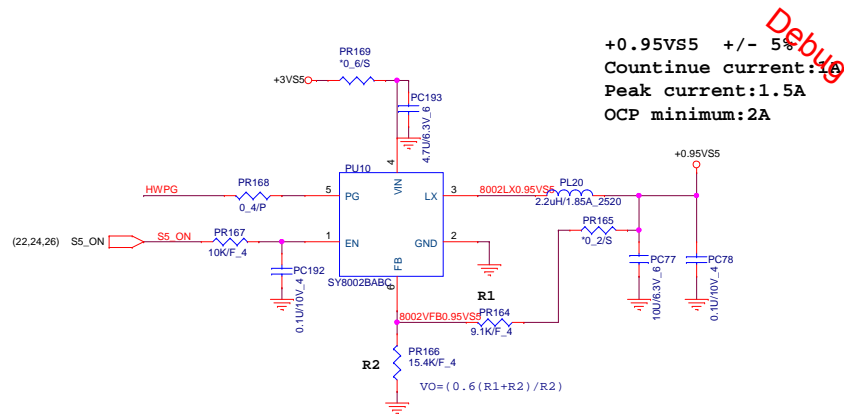
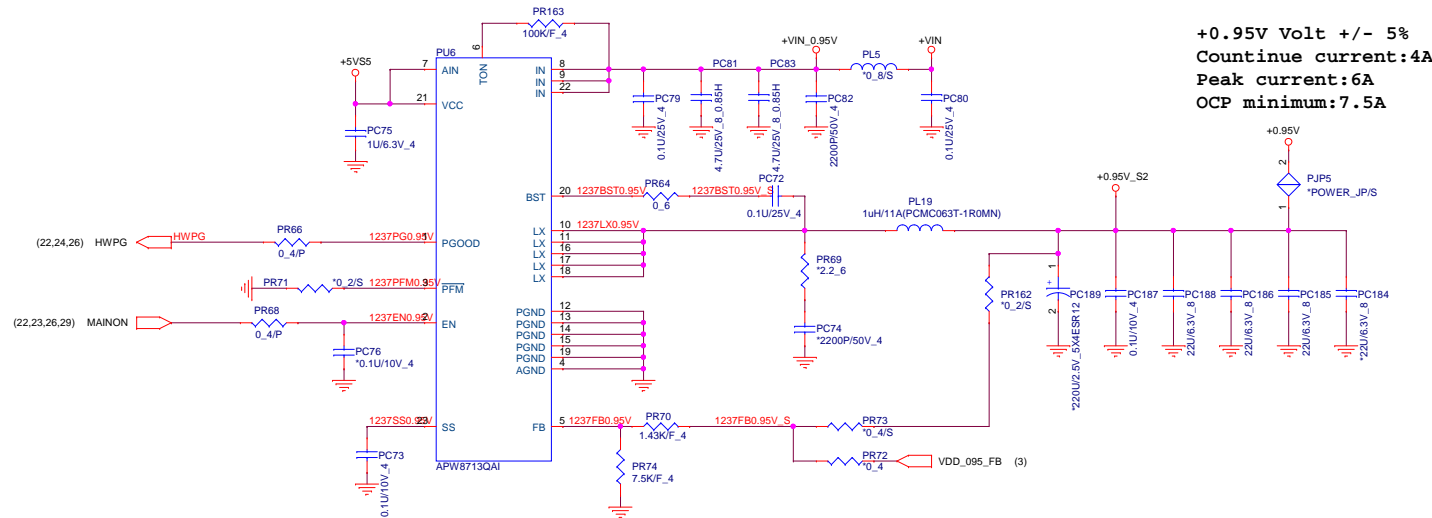



		PROJECT : Y02	
		Quanta Computer Inc.	
Size Custom	Document Number	Charger (OZ8691ALN)	
Date: Tuesday, July 02, 2013	Sheet	23	of 30
Rev		1A	

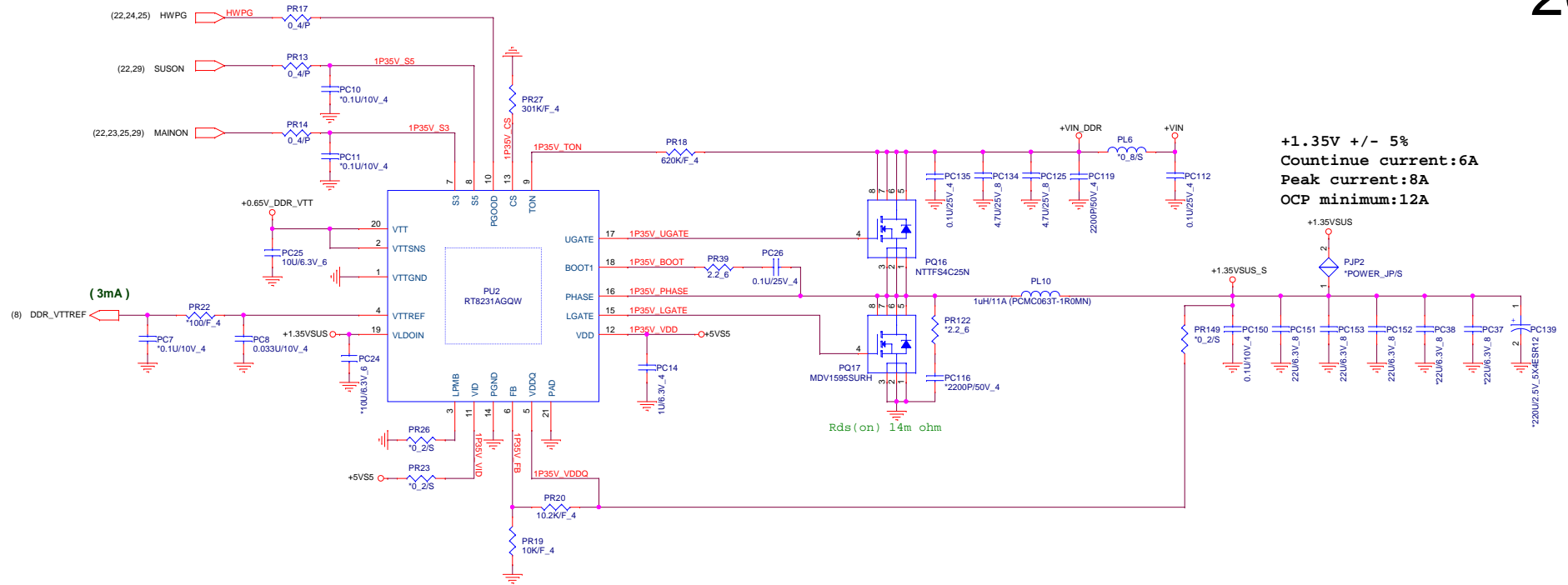
DC/DC +3VS5/+5VS5



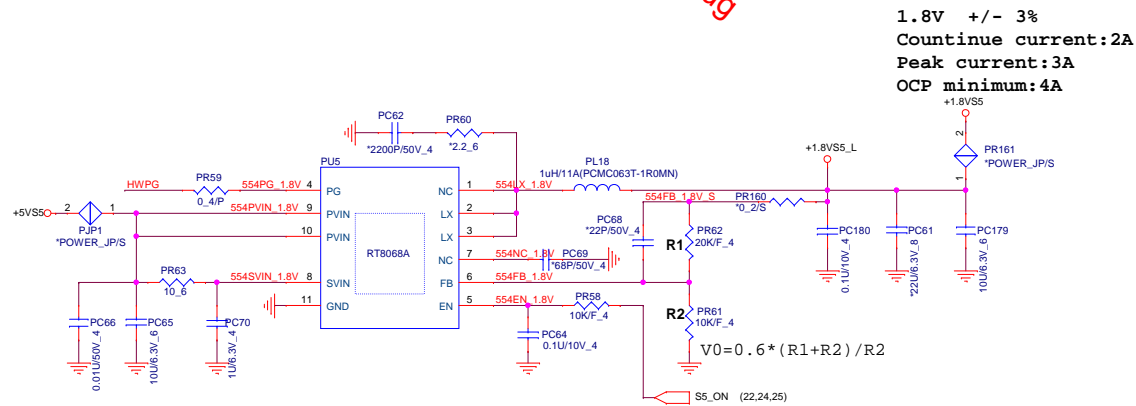
		PROJECT : Y02	
		Quanta Computer Inc.	
Size	Document Number	3/VS5 (NB670/NB669)	Rev 1A
Custom			
Date:	Tuesday, July 02, 2013	Sheet	24 of 30




 PROJECT : Y02 Quanta Computer Inc.		
Size Custom	Document Number +0.95V(A0Z1237)/+1.5V5	Rev 1A
Date: Tuesday, July 02, 2013	Sheet 25 of 30	

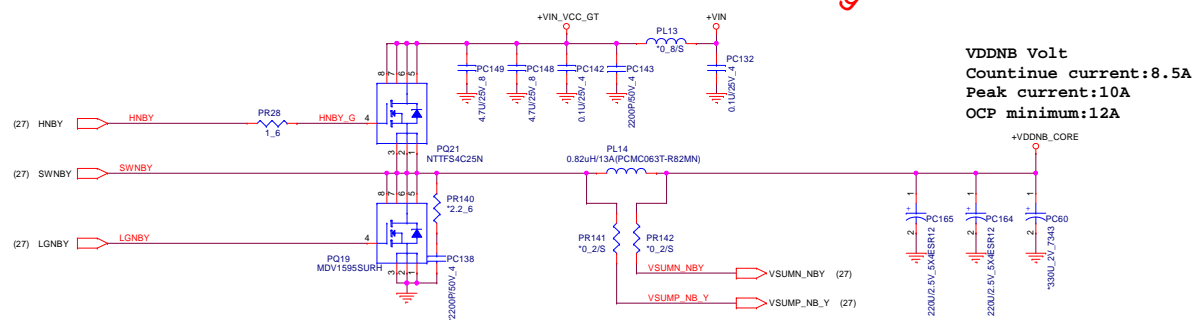
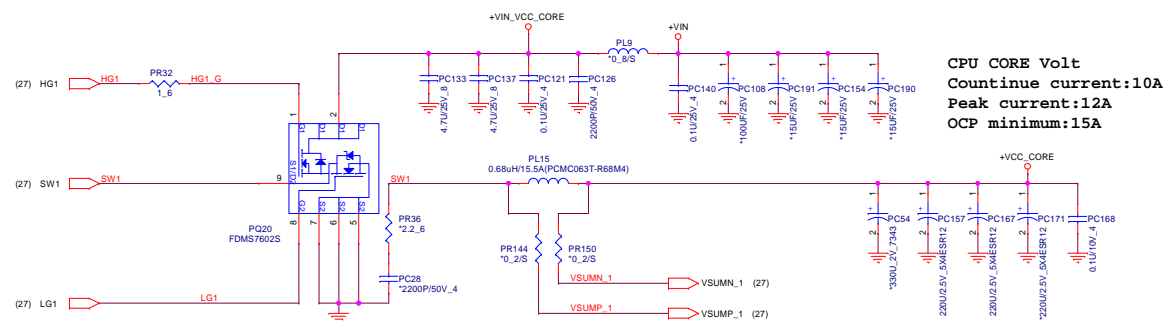


Debug

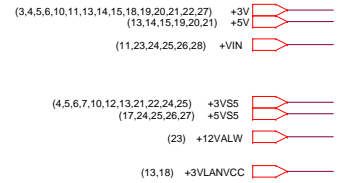
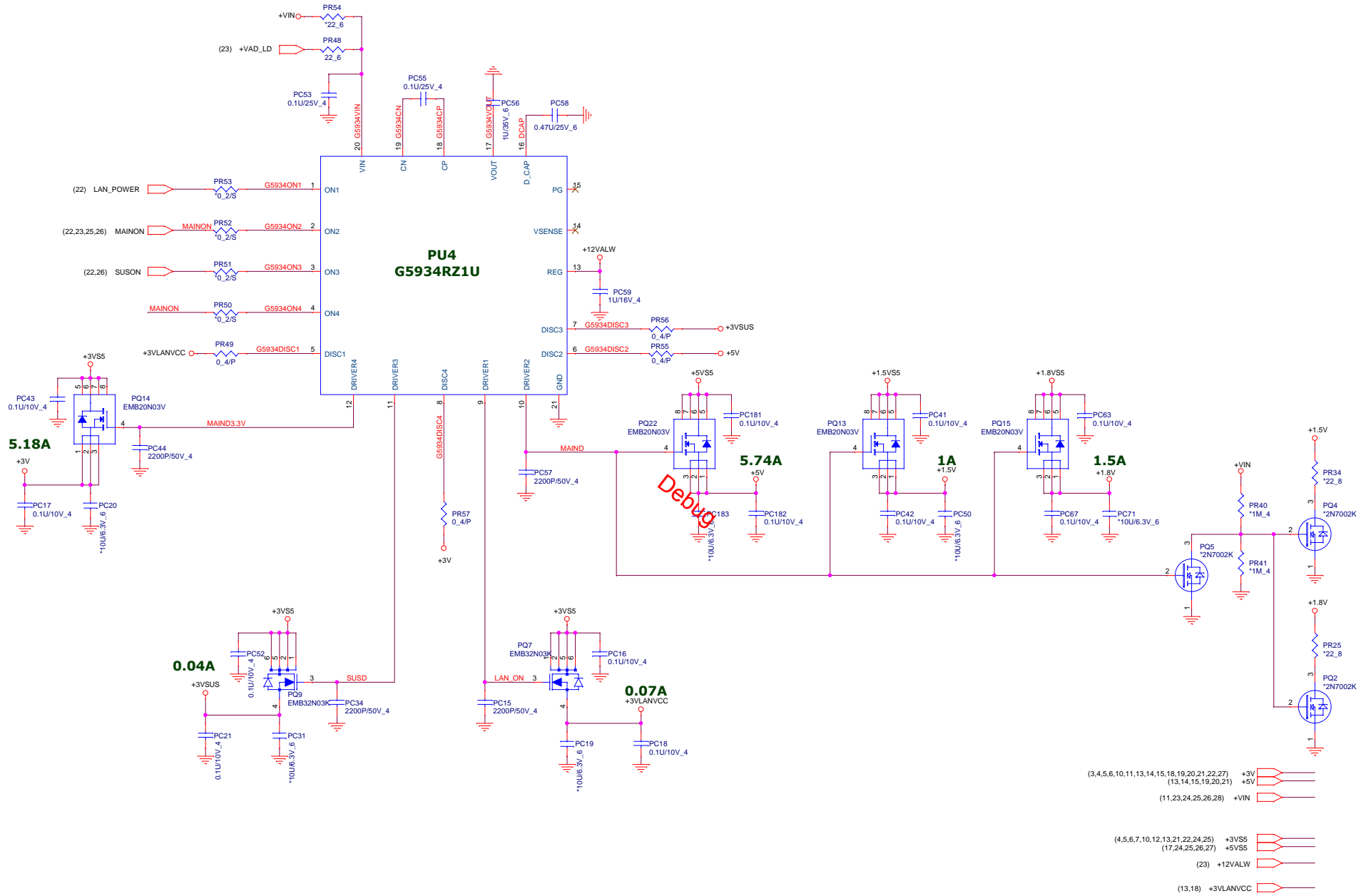



+1.35VSUS (2,6,8,9)

		PROJECT : Y02	
		Quanta Computer Inc.	
Size	Custom	Document Number	DDR3 (RT8231A)/1.8VS5
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Debug



		PROJECT : Y02	
		Quanta Computer Inc.	
Size Custom	Document Number	Dis-charge IC (G5934)	Rev 1A
Date: Tuesday, July 02, 2013		Sheet 29 of 30	